

Double-headed arrows indicate angular orientations of currents flowing through channels of TFTs.

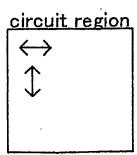
# SKETCH 1

Configuration of Claim 2 of Invention

first circuit region	second circuit region
$\begin{array}{ccc} \leftrightarrow & \leftrightarrow \\ \leftrightarrow & \leftrightarrow \\ \leftrightarrow & \leftrightarrow \\ \end{array}$	\\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\

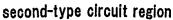
# SKETCH 2

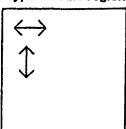
Configuration of Fig. 2 of Yamauchi



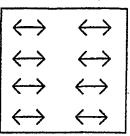
#### SKETCH 3

Configuration of Claim 3



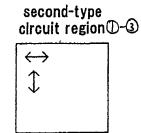


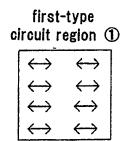
first-type circuit region

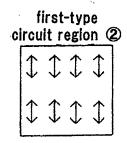


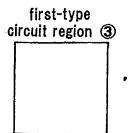
### SKETCH 4

Configuration of Claim 3 includes 3 pairs of circuit regions









## SKETCH 5

Configuration of Claim 4 includes 3 pairs of circuit regions

